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JC072 U.S. PRO

	Class	Subclass	ISSUE CLASSIFICATION
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**PATENT NUMBER**

**U.S. UTILITY Patent Application**

O.I.P.E. Q2 053 SCANNED	PATENT DATE Q.A. Ans	
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APPLICATION NO. 09/854146	CONT/PRIOR D	CLASS 438	SUBCLASS -	ART UNIT 2825 <del>2812</del>	EXAMINER Sjek
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## High accuracy timing model for integrated circuit verification

## TITLE

PTO-2040  
12/89

**ISSUING CLASSIFICATION**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) 			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	(Primary Examiner) 			ISSUE FEE	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	(Legal Instruments Examiner) 			ISSUE BATCH NUMBER	

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